


	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	21	-	20	=	1	\$18	\$18.00
INDEPENDENT	5	-	3	=	2	\$84	\$168.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$186.00

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☐ No fee is required.
- ☒ Check(s) in the amount of \$186.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Charles Gorenstein, #29,271

CG/CTB/kdb
1248-0538P

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

ATTACHMENT

(Rev. 09/27/01)



7/a
Step 1
7-12-02
PATENT
1248-0538P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eiji YANAGAWA et al. Conf. No.: 4277
Appl. No.: 09/820,671 Group: 2815
Filed: March 30, 2001 Examiner: S. Clark
For: SEMICONDUCTOR DEVICE & METHOD OF
MANUFACTURING THE SAME

RECEIVED
JUL - 8 2002
TECHNOLOGY CENTER 2800

AMENDMENT

Honorable Commissioner for Patents
Washington, D.C. 20231

July 1, 2002

Sir:

In response to the Office Action dated April 1, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please replace claims 1 and 4-9 with the following amended claims: Please note that claims 1 and 4-9 are presented below in their amended form. They are further presented as an Attachment to the Amendment whereby the amendments to the claims are outlined using the conventional method of bracketing and underlining.

506B1
C1
cont

1. (Amended) A semiconductor device, in which a semiconductor element with an integrated circuit is secured to a board,
wherein the semiconductor element is secured in a level position and specified to operate normally only when the semiconductor element is maintained in this level position,

07/03/2002 MAHME1 00000043 09820671

01 FC:102
02 FC:103

168. 0P
18.00 0P